# A Proposal to China; Synch TDMA DSB Network for SINOSAT

Osamu Ichiyoshi

Human Network for Better 21 Century; <u>http://www5e.biglobe.ne.jp/~kaorin57/</u> eMail; <u>osamu-ichiyoshi@muf.biglobe.ne.jp</u>

# Abstract

China is a vast country made up of many distant regions full of varieties and differences. The satellite communication is quite suitable for China because it can give an easy, universal and economical coverage of the whole country. The direct satellite broadcasting (DSB) can give a direct reception of the broadcast signal from the satellite anywhere under the coverage of the satellite. The conventional DSB systems are STAR networks; all the channels signals are transmitted from a single or a few central stations. This narrow transmit side structure of the DSB limits the range of the provided service to only one-way flow of the information. The constraint can be eliminated with the Synchronous TDMA DSB proposed herein. The Synch TDMA DSB allows multiple transmissions of the broadcast signals with small satellite earth stations anywhere and can be received with conventional DSB receivers everywhere under the coverage of the satellites. The wide area, single hop and direct access features of the satellite communications can be fully utilized. The system will be useful for China to promote more versatile and interactive information exchanges from remote areas to the whole country.

### 1. SINOSAT

China operates advanced satellites systems covering the whole China and neighboring Asian regions. In this paper we study Sinosat satellites disclosed in the following URL; <u>http://www.sinosatcom.com/english/index.htm</u>

# 12. Synchronous TDMA and Its Application to DSB Network

### 2-1. TDM and TDMA

In digital DSB systems a number of different channels signals are multiplexed in Time Division Multiplex (TDM) mode to form Transport Stream (TS) for transmission to the satellite. All signals can be transmitted from a single station and can be received anywhere within the coverage area of the satellite. This central structure of the network does not enable direct transmission from multiple sites and strictly limits the application fields of the network to conventional, one way and star network broadcasting.

The Time Division Multiple Access (TDMA) allows direct, simultaneous and independent transmission from multiple sites to the same satellite transponder. The stations share the same transponder in TDMA mode. Each station transmits a burst of finite time length. In order to avoid collision of bursts from different stations, each station conducts Burst Timing Control. The TDMA can be classified into two types; asynchronous and synchronous. In the asynchronous system given are Guard Time (GT) of a few symbols before and after each burst and the burst timing control is made to contain the burst within the GT. Timing error can exceed a few symbols so long as the burst is contained within the GT. In the Synchronous TDMA, all burst are fully synchronized to the phase of the symbol clock hence no guard time greater than half s symbol becomes unnecessary. In synchronous TDMA all signals are fully synchronized and look as if sent from the same station hence the signal can be received with conventional DSB receivers everywhere. Table 2-1 gives a list of the historical satellite TDMA systems

Syste	Development	Frame	Data rate	Clock	Notes
J			Data Tale		notes
m	Time and body	length		Synch. mode	
SMAX	1967-72 NTT Public Corporation	125 (µsec)	13.664(MHz)	Synch.	Verified feasibility of Synch TDMA by joint satellite experiment between Japan and USA
Sub-Mil-	1974-82	328 (µsec)	64(Mbps)	Synch.	Commercialized as back-up
Wave	NTT Public		BPSK		for 8 RC stations, the highest
TDMA	Corporation				hierarchy in telephony nwk.
Remote	1974-82	105( µ sec)	105(Mbps)	Synch.	Provided 192 telephony and 2
Islands	NTT Public		QPSK		TV channels to Ogasawara
TDMA	Corporation				Islands
Intelsat	1975-84	2 (ms)	120(Mbps)	Asynch.	International communication
TDMA	Intelsat		QPSK	-	
Others	Various TDMA systems were developed in Japan, Europe, USA and Canada for varieties of				
	objectives. All systems adopted Asynchronous methods for burst control.				
DSB	2003—	30 (ms)	42.192(Mbps)	Synch.	This proposal
TDMA	Proposed	(TV frame)	QPSK	-	
	*	. ,	(DVB-S spec)		

### Table 2-1 Parameters of various TDMA systems

# 2-2. Application of Synchronous TDMA to DSB

Basic functions;

- (1) Up to typically 10 earth stations (ES) share a satellite transponder (capacity; 30Mbps) in Synch.TDMA mode.
- (2) A Reference earth Station (RES) transmits Reference Burst generated at its stable time base.
- (3) All other Feeder Earth Stations (FES) synchronize their bursts with the RES burst by satellite loop PLL.
- (4) The RES and FES bursts combine at the satellite to form a clock coherent downlink signal.
- (5) The clock coherent and continuous downlink signals can be received with existing DSB receivers.

### Features

- (1) Direct transmission to the satellite from multiple sites can be applied to conventional DSB systems.
- (2) The video and audio coding techniques for the satellite and terrestrial systems are common, hence complimentary development of the satellite and terrestrial broadcast networks becomes possible.

### 2-3. TDMA Frame structure

The multiplex format in the proposed system follows DVB specification.

[1] Frame period - 30 (ms) ; Frame frequency of TV ; 29.97 Hz

[2] Data rates;

(1) Modulation ; 42.192Mbps

(2) Information (Mbps) ; 19.4 (1/2), 25.9 (2/3), 29.2 (3/4), 32.4 (5/6), 34.0 (7/8) (Punc. Ratio of conv. codes)

[3] Multiplex method

(1) A number N of TS packets (188 bytes) form a burst. N is allocated to the FES by the system.

(2) The Reed-Solomon coding is applied based on DVB-S specification to form 204-byte TS packets.

(3) Let the nominal convolutional coding puncture ratio 3/4, then

TS packet length =  $204 \times 4/3 = 272$  (bytes) = 2176 (bits)

- (4) The burst is composed of multiples of 8 packets; processing unit for FEC puncturing and randomization
- (5) Example TDMA frame

Number of packets per frame ; 648 (1,410,048 bits)

Frame frequency ; 42.192 (MHz) / Number of bits = 29.92 (Hz)

[4] Burst format

- (1) The first packet in a burst contains fixed patterns for carrier and clock recovery at the receivers.
- (2) Subsequent packets can fully carry information data.
- (3) The randomization PN code is initialized at the start of every 8 packets.
- (4) Some of the final 12 packets can not carry information data because of interleave processing. But they can be used for inter-ES communication.

# 3. Link Power Budget

An example of the power link budgets for SINOSAT-1,3 and Japanese JCSAT-3 are depicted in Table 3-1.

Table 3-1 Link Power Budget

Item	SINOSAT-3	SINOSAT-1 Japan			
Uplink	C bands (6GHz)	C bands (6GHz) Ku bands (14GHz)		Ku bands (14GHz)	
Feeder Earth Station					
HPA output power (dBW)	23 (200 Watts)	20 (100 Watts)	13 (20 Watts)		
TX Antenna diameter (m)	2.4	2.4	1.2		
Transmit Antenna Gain (dBi)	40.5	48	43		
Feeder loss (dB)	0.5	0.5	0.5		
EIRP (dBW)	63.0	67.5	55.5		
Free Space Loss (dB)	200.0	207.6	207		
Satellite					
G/T (dB/K)	0	0	10		
Boltzman constant k (dB)	-228.6				
Uplink C/No (dB/Hz)	91.6	88.5	87.1		
Downlink	C bans (4 GHz)	Ku bands (12GHz)	12 GHz		
Satellite EIRP (dBW)	40	46	54.5		
Free Space loss (dB)	196.5	206.1 (12 GHz)	206		
Receiver			UT	ES	
Antenna diameter (m)	2.4	2.4	(0.5m)	(1.2m)	
Antenna Gain (dBi)	37.0	46.6			
Noise temperature (K)	200	200			
G/T (dB/K)	14.0	23.6	10	17	
Bolzman constant k (dB)	-228.6				
Downlink C/No (dB/Hz)	86.1	92.1	87.1	94.1	
Overall C/No (dB/Hz)	85.0	86.9 84.1		86.3	
Data Rate (dB.Hz) 42.192Mbps	76.3	76.3 76.3			
Eb/No (dB)	8.7	10.6 7.8		10.0	
Operation Eb/No (dB)	5.0	5.0 5.0			
Link margin (dB)	3.7	5.6	2.8	5.0	

# 4. Methods and Performances of Satellite Loop Clock Synchronization

# 4-1. TDMA Signal Multiplex on the Satellite

Multiple Feeder Earth Stations (FES) send data bursts signals to a common satellite transponder. The signals are combined on the satellite to form a single sequence for the downlink signal. The bursts timing is controlled to avoid collision with the bursts from other FES. One of the earth stations is designated as a reference earth station (RES). The RES transmits a reference burst which defines the TDMA frame. Then each FES transmits its data burst to the assigned time slots on the TDMA frame. The combined frame is depicted in the following figure.

]	Reference burst	FES i burst	FES j burst	FES k burst	Reference burst	
		rame				

### Figure 4-1 TDMA frame structure

# 4-2. Clock Synchronization System

Every FES must synchronize its transmit clock with the RES clock on the satellite. The downlink signal then becomes a clock coherent signal. The FES receives the downlink signal which contains all bursts from the FESs., The clock components are generated by square law detection of the receive signals. The FES then regenerates a continuous RES clock from the detected RES bursts clock components by a receive phase lock loop (Rx PLL). The recovered RES clock gives the clock reference at the FES. The FES compares the phase of its own square law detected burst clock with the clock reference. The detected phase error is then smoothed by Loop Filter and control the transmit clock generating voltage controlled oscillator (Tx VCO). The output of Tx VCO is used to generate the transmit burst signal to be sent to the satellite. The transmit phase lock loop (Tx PLL) contains the long satellite loop delay hence must be of a very narrow bandwidth for loop stability. The structure of the clock synchronization system is depicted in Figure 4-2.

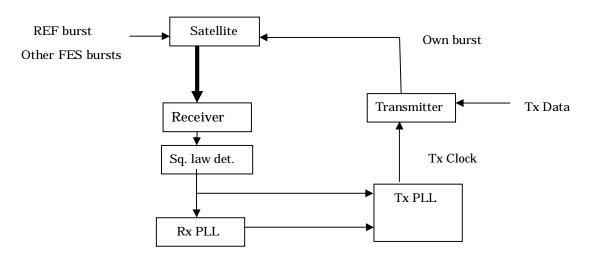


Figure 4-2 Structure of Clock Synchronization System

#### 4-3 Rx PLL

The structure of the Reference Clock recovery Phase Lock Loop (Rx PLL) is depicted in Figure 4-3.

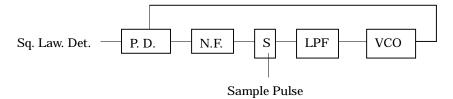


Figure 4-3 Structure of Reference Clock recovery PLL (Rx PLL)

The amplitude of a QPSK modulated signal changes with the modulating signal, hence a square law detector can regenerate the clock components. The Rx PLL compares the phase of Rx Clock VCO with the Square Law Detector output. The phase comparator output is smoothed by noise rejection low pass filter (Noise Filter) before a sampler. The sample timing shows the end of the reference burst (Ref burst). The sampled value is held until the next sampling. The sample & held value is smoothed by Loop Filter (LPF) to control the Rx Clock VCO.

The sampling frequency is the TDMA frame frequency which is 30 Hz. The bandwidth of the RX PLL is designed to be around 1/10 of the sampling frequency, or 3 Hz. A significant S/N degradation can occur at the Noise Filter and Sampler. The sampling causes the spectrum fold-over which increases the noise density by the ratio [Bandwidth of Noise Filter] / [Sampling frequency]. We specify the burst length be at least 1/10 of the frame then we have at most 10 dB degradation of the noise density. Another S/N degradation of about 10dB occurs at the square law detector. All the other processing are linear operations. The ratio of approximately 30 MHz of the receive signal over the Rx PLL equivalent noise bandwidth of 3 Hz gives 70 dB S/N improvement. Deduction of the above degradation gives about 50 (dB) S/N improvement by the Rx PLL. Even when the input S/N is 0 dB, the Rx PLL output S/N becomes 50 db; (the variance of) the phase error is only 0.13 degree.

# 4-4. Tx PLL

The structure of Transmit Clock generating Phase Lock Loop (Tx PLL) is depicted in Figure 4-4. The structure of Tx PLL is the same as Rx PLL except that the sample pulse shows the end of its own burst and the PLL includes the long satellite loop delay.

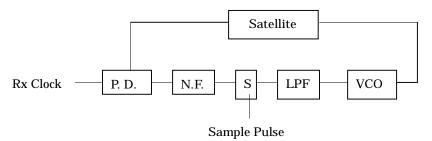


Figure 4-4 Structure of Transmit Clock PLL (Tx PLL)

The satellite loop delay is about 0.27 sec hence the bandwidth of Tx PLL must be designed to be narrow enough to guarantee the stability of the PLL.

Based on the previous works [1],[4], the following loop parameters are adopted;

Loop parameters;

Natural frequency	;	n = 0.924 (rad/sec)
Damping factor	;	= 0.707
Equivalent noise bandwidth	;	BL= 0.49 (Hz)
Loop gain	;	Ko = 290 (/sec)
LPF time constant	;	To = 340 (sec)
Satellite Loop Delay	;	= 0.27 (sec)

Loop stability;

The Bode diagram analysis on the Tx PLL with the above parameters gives the following margins;

Phase Margin	; 43 (degrees) at $/ n = 1.4$
Gain margin	; 12 (dB) at / n =19.0

VCO Phase Noise

For such a narrow band PLL, the resultant phase error is dominantly caused by the phase noise within the VCO. The phase noise is characterized by phase noise power density spectrum /(j)/2 which generally has the following components..

 $(j)/^{2} = /^{2}Ba + / ^{2}A + / ^{3}$ 

The first term is the additive noise component with bandwidth Ba and the total phase noise power  $\$ , the second term is the random walk noise and third is 1/f noise components.

Another frequently used characterization of the oscillator phase noise is the average phase drift over time  $\$ , which is defined as follows. Let the phase of the oscillator (t), then the average time drift () is defined as  $/()^2 = \langle (t) - (t-) \rangle^2 \rangle$ , where  $\langle x \rangle$  means the time average of x.

It can be shown that the above two expressions are related as follows;

/ ()/^2 = + . + . ^2

The frequency stability function S( ) of an oscillator with frequency fo is defined by

S() = () / (2 fo.)

For large , the phase drift is dominantly caused by 1/f noise and the frequency stability of the oscillator tends to be constant;

 $S() = () / (2 f_0)$ 

Tx PLL phase error

It can be shown the resultant phase error in PLL with damping factor = 1/(2) is given by the following formula;

/ e/^2 = + 3 /16 /BL + 9 /128 /BL^2

The phase noise performance of crystal oscillators today is quite good. For example, a commercial VCXO (ex. 7100 series , NDK) gives the following performance

 $(j)/^{2} = -50 (dBc/Hz)$ at 1(Hz)

which gives

$$= 2.5 \times 10^{-3}$$

< 4.0 x 10^-5

Then the resultant phase error of Tx PLL with BL = 0.49 (Hz) is

/ e/ = 0.027 (rad) = 1.57 (deg)

Thus a very accurate synchronous TDMA is technically feasible.

# 5. Comparison with SkyPlex system

In Europe a system called SkyPlex is in operation. The objective of this proposed system is the same as that of SkyPlex In Europe there are many countries with different languages within the coverage areas of the communication satellites. With development of EU people move to different countries more freely. There arise naturally demands for a DSB system that enables direct transmission to the satellite from many different countries and the combined signal can be received by conventional DSB receivers all over Europe. In SkyPlex the uplink signal is in single channel per carrier (SCPC) in frequency division multiplex (FDM) mode. The uplink signals are demodulated and the data sequences are regenerated on the satellites. The regenerated signals are re-multiplexed into time division multiplex (TDM) format as specified in the DSB system. The SkyPlex system allows a very small FES because a single channel is sent in a continuous mode and achieves a perfect compatibility with conventional DSB system. However, the SkyPlex system requires a special and fairly complex satellite. On the other hand the proposed Synchronous TDMA system is applicable to simple, bent-type satellites which are available all over the world.

# 6. Social benefits of the Synchronous TDMA DSB network

The proposed system is effective to provide a universal broadcasting and communication network for the wide areas covered by the satellites. Local industries can promote their products to the wide area markets through the DSB network. It will be effective to vitalize the local industry and develop national economy. It will also bring the nationwide broadcasting network much closer to the general public. A new industry will arise that will provide public access with the nationwide broadcasting network for local industries, governments, various groups and even individuals. The broadcasters may evolve to BSP corresponding to ISP in the Internet. Integration of the proposed network with the Internet realizes the Internet with full broadcast capability or an interactive nationwide broadcast network accessible even from the users' homes.

The FES can function as a Gateway Station for the local communication or broadcasting network. The Gateway can be Toll Switch in local telephony network, where the communication satellites provide the long distance trunk links in the sky. The nationwide broadcasting and communication can be integrated quite naturally. For example, 700 telephony and 2 TV channels can be carried through a transponder with a 30 Mbps capacity. The system will be effective for such areas where there are many remote islands or deep mountain areas. The system concept is depicted in the following figure.

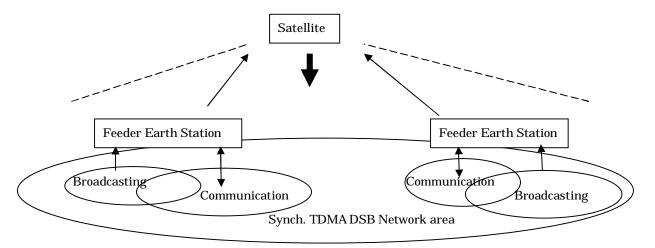


Figure 6 Integrated Communication and Broadcasting Network

# Conclusion

The proposed system can realize a truly direct satellite broadcasting network for the transmitter sides as well as the receiver sides. The Synchronous TDMA is a field proven technology put into practice in Japan about twenty years ago. The wide area, single hop, direct accessibility features of the satellite communication networks can be fully utilized. The DSB network can evolve from conventional, centralized and one way STAR structure onto new, distributed and interactive MESH structure. The geographical universality and easy installation of the system is effective to integrate remote and local networks into the wide area broadcasting and communication networks. The universality of the system will be effective to vitalize local industries and national economy and will be useful for developing and developed countries as well.

# References

- [1] Special Issues on Domestic Satellite Communication Developments (20 papers, in Japanese),
  Electrical Communication Laboratories Technical Journal, Vol. 29 No.4, 1980
  Nippon Telegraph and Telephone Public Corporation
- [2] Patent application (Japan) 2005-151472, " Synchronous DVB/TDMA satellite communication network"
- [3] SkyPlex; <u>http://www.esa.int/esaCP/SEM6YM2PGQD\_index\_0.html</u>
- [4] D.Kumagai, H.Doi, M.Takada, "SMAX-PCM Multiple Access Satellite Communication System" Journal of IEICE vol. 53, No. 2, 1970 (in Japanese)